
Low Power Devices and Technology

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The Journal may also include the computation capability of the memory operations which are used in database related seems to be increasingly bound in the recent years. The memory-related operations starting from hard disk drive based system to higher bandwidth memory technologies such as in-memory, non-volatile memory etc. face consequences. The transition of such with higher bandwidth especially in memory is crucial. One of the solutions for such an evolution is hardware acceleration. Field Programmable Gate Array (FPGA) is selected among other hardware accelerators ranging from embedded device to cloud computing because of its higher performance, energy efficiency and adaptability.

Reduced Instruction Set Computer (RISC) is one of the most common types of architecture involved in microprocessors that has several blocks. There is a lot of scope that can be observed in optimizing these blocks involved in RISC resulting in better and effective microprocessors.

As a result, article publications in all computer science-related fields are included. Additionally, it publishes articles to analyses the research, innovation, and entrepreneurial ecosystems of a nation, region, or industry. It is invited to submit papers in areas with a current focus, such as open science, innovative tools and methodologies, metrics, and novel publication formats, among others. The journal accepts articles in a variety of genres; for further information, see the guidelines to authors.